FORM 1082

Sheets of drawing.
 An assignment of the invention to ______.
 A Declaration and Power of Attorney - UNEXECUTED.
 A verified statement to establish small entity status under 37 CFR 1.9 and 37 CFR 1.2
 Claim to Right of Priority w/certified copy of <u>Japanese</u> application no(s). <u>10-301128</u>.
 An Information Disclosure Statement.
 The filing fee has been calculated as shown below:

OTHER THAN A SMALL ENTITY

File Number: MTS-V03174

	(Col. 1)	(Col. 2)
FOR:	No. Filed	No. Extra
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Total	7-20=	0*
Claims		
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Please charge my Deposit Account No.18-0350 the amount of \$. A duplicate copy of this sheet is enclosed. A check in the amount of \$760 to cover the filing fee is enclosed. The Assistant Commissioner for Patents is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 18-0350 . A duplicate copy of this sheet is enclosed. Ø Any additional filing fees required under 37 CFR 1.16. M Any patent application processing fees under 37 CFR 1.17. The Assistant Commissioner for Patents is hereby authorized to charge payment of the following fees during the pendency of this application or credit any overpayment to Deposit Account No. 18-0350. A duplicate copy of this sheet is enclosed. Any patent application processing fees under 37 CFR 1.17. The issue fee set in 37 CFR 1.18 at or before mailing of the Notice of Allowance, pursuant to 37 CFR 1.311(b). Any filing fees under 37 CFR 1.16 for presentation of extra claims. RATNER AND PRESTIA Respectfully submitted, Suite 301,

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Kathleen Libby

anuil N. Calde

Allan Ratner, Reg.No.19,717

Daniel N. Calder, Reg. No. 27,424

One Westlakes, Berwyn

Valley Forge, PA 19482-0980

P. O. Box 980

(610) 407-0700

TRANSMITTING/RECEIVING SWITCH AND PORTABLE TERMINAL UNIT

BACKGROUND OF THE INVENTION

Field of the Invention

This invention relates to a transmitting/receiving switch and a portable terminal unit that are used to switch high-frequency signal transmission lines such as a high-frequency circuit in a radio transceiver or a portable terminal unit.

Description of the Related Art

Referring to FIG. 5, there is a block diagram of one example of a conventional transmitting/receiving switch. The shown example includes an antenna terminal 501 connected to an antenna ANT, a transmission terminal 502 connected to a transmission circuit Tx, a reception terminal 503 connected to a reception circuit Rx and a control terminal 504 connected to a control circuit CONT that performs transfer switching controls. The transmission terminal 502 is connected to the anode of a first diode 506 through a first capacitor 505. The cathode of the first diode 506 is connected to the antenna terminal 501 through a node 507 and a second capacitor 508. The antenna terminal 501 is also connected to the reception

terminal 503 through a second capacitor 508, a node 507, a quarter-wave line 509 and a third capacitor 510. Furthermore, the anode of the first diode 506 is also connected to the control terminal 504 through a coil 511 and a resistance 512. A midpoint between the coil 511 and the resistance 512 is grounded through a fourth capacitor 513. The anode of a second diode 514 is connected to the midpoint between the quarter-wave line 509 and the third capacitor 510, and the cathode of said second diode 514 is grounded.

Referring to the transmitting/receiving switch shown in FIG. 5, in case of connecting the transmission circuit Tx with the antenna ANT, a positive voltage is supplied from the control circuit CONT to the control terminal 504 that turns on the first diode 506 and the second diode 514. At this time, the direct-current component of the positive voltage supplied from the control terminal 504 is blocked by the first through fourth capacitors 505, 508, 510 and 513, and thus the positive voltage is applied to only the partial circuit that includes the first diode 506 and the second diode 514. Said positive voltage drives the first diode 506 and the second diode 514 to the ON state. In this case, the ON state of the first diode 506 lowers the impedance between the transmission terminal 502 and the antenna terminal 501 to establish a connection between them. Meanwhile, the ON state of the second diode 514 brings the quarter-wave line 509 grounded at high

frequencies and makes the impedance from the node 507 toward the reception terminal 503 significantly high. As a consequence, an input signal from the transmission terminal 502 is transmitted to the antenna terminal 501 without leaking into the reception terminal 503.

Also referring to the transmitting/receiving switch shown in FIG. 5, in case of connecting the reception circuit Rx and the antenna ANT, a null voltage is supplied from the control circuit CONT to the control terminal 504. Under this condition, the first and second diodes 506 and 514 are driven to the OFF state and the impedance from the node 507 toward the transmission terminal 502 becomes significantly high. The reception terminal 503 and the antenna terminal 501 are connected through the node 507, quarter—wave line 509 and the third capacitor 510. As a consequence, input signals from the antenna terminal 501 are transmitted to the reception terminal 503 without leaking into the transmission terminal 502.

As described above, the conventional transmitting/receiving switch performs reception/transmission switching by supplying a positive voltage or null voltage to the first and second diodes 506 and 514. The conventional art transmitting/receiving switch suffers from a problem when high-power transmission signal is imposed on the transmission terminal 502: the transmission

signal is distorted due to the non-linearity of the switching element of the first diode 506; harmonic contents are produced in the transmission signal; said harmonic contents are transmitted to the antenna terminal 501 and radiated from the antenna ANT.

BRIEF SUMMARY OF THE INVENTION

One aspect of the present invention is a transmitting/receiving switch that solves above described problem. A transmitting/receiving switch according to the present invention attenuates harmonic contents in the transmission signal produced by the distortion of the transmission signal due to the non-linearity of the switching element, without increasing the reception loss. Another aspect of the present invention is a portable terminal unit having a transmitting/receiving switch of the present invention.

The first invention of the present invention is a transmitting/receiving switch comprising:

an antenna terminal connected to an antenna;

- a transmission terminal connected to a transmission circuit;
 - a reception terminal connected to a reception circuit;

a first switching element that turns on or off the connection between said antenna terminal and said transmission terminal;

a second switching element that turns on or off the connection between said antenna terminal and said reception terminal; and

filtering means inserted between said first switching element and said antenna terminal,

wherein said filtering means (1) attenuates harmonic contents in the transmission signal and (2) has phase characteristics that increases the impedance from the node between said antenna terminal and said second switching element toward said transmission terminal in the reception frequency band.

The second invention of the present invention is the transmitting/receiving switch as set forth in said the first invention, wherein said filtering means comprises:

a filter that attenuates harmonic contents in said transmission signal; and

a phase-shift circuit that performs phase adjustment on the basis of said phase characteristics according to the amount of phase shift caused by said filter.

The third invention of the present invention is the transmitting/receiving switch as set forth in said the first or second invention, wherein said first switching element

comprises a diode and said second switching element comprises a diode and a quarter-wave line.

The fourth invention of the present invention is the transmitting/receiving switch as set forth in said the second invention, wherein said filter comprises a low pass filter that attenuates the harmonic contents in the transmission signal.

The fifth invention of the present invention is the fitransmitting/receiving switch as recited in said the second invention, wherein said phase-shift circuit comprises either (1) a strip line or (2) a lumped constant circuit having a coil and/or capacitor.

The sixth invention of the present invention is a portable terminal unit comprising:

an antenna for radio transmission and radio reception;
a transmitting/receiving switch as recited in said the
first invention that switches transmission lines through
which signals flow during said radio transmission or radio
reception;

a transmission circuit connected to said transmission terminal of said transmitting/receiving switch; and

a reception circuit connected to said reception terminal of said transmitting/receiving switch.

BRIEF DESCRIPTION OF THE DRAWINGS

- FIG. 1 is a block diagram of a first embodiment of the transmitting/receiving switch according to the present invention;
- FIG. 2 is a block diagram of a second embodiment of the transmitting/receiving switch according to the present invention.
- FIG. 3 is a graph of the passing characteristics of the second embodiment of the present invention at the time of transmission;
- FIG. 4 is a graph of the impedance characteristics of the second embodiment of the present invention at the time of reception, indicating the impedance from the node toward the transmission terminal in the reception frequency band; and
 - FIG. 5 is a block diagram of a conventional transmitting/receiving switch.

[Description of Symbols]

101, 201, 501 Antenna terminal

102, 202, 502 Transmission terminal

103, 203, 503 Reception terminal

104 First switching element

105 Filter

106, 208 Phase-shift circuit

107, 209, 507 Node

108 Second switching element

204, 504 Control terminal

205, 505 First capacitor

206, 506 First diode

207 Low pass filter

210, 508 Second capacitor

211, 509 Quarter-wave line

212, 510 Third capacitor

213, 511 Coil

214, 512 Resistance

215, 513 Forth capacitor

216, 514 Second diode

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Preferred embodiments of the transmitting/receiving switch according to the present invention will now be described referring to the drawings.

(First embodiment)

FIG. 1 shows a block diagram of a first embodiment of the transmitting/receiving switch according to the present invention.

The first embodiment of the transmitting/receiving switch according to the present invention comprises an antenna terminal 101 connected to an antenna ANT, a

transmission terminal 102 connected to a transmission circuit Tx and a reception terminal 103 connected to a reception circuit Rx.

The transmission terminal 102 is also connected to the antenna terminal 101 through a first switching element 104, a filter 105 that passes transmission signals, a phase-shift circuit 106 and a node 107.

The antenna terminal 101 is also connected to the reception terminal 103 through the node 107 and a second switching element 108. In this configuration, a filter 105 and a phase-shift circuit 106 configure a filtering means.

Operation of the transmitting/receiving switch configured as above will be described below.

In case of connecting the transmission circuit Tx with the antenna ANT, the first switching element 104 is turned on and the second switching element 108 is turned off.

Under this condition, when the transmission signal is fed through the transmission terminal 102 the impedance from the node 107 toward the reception terminal 103 in the transmission frequency band is significantly high because the second switching element 108 is in the OFF state. Therefore, the transmission signal fed through the transmission terminal 102 is transmitted to the antenna terminal 101 through the first switching element 104, the filter 105, the phase-shift

circuit 106 and the node 107 without leaking into the reception terminal 103.

In this case, the filter 105 attenuates harmonic contents in the transmission signal produced by the distortion of the transmission signal due to the non-linearity of the switching element of the first switching element 104.

In case of connecting the antenna ANT with the reception circuit Rx, the first switching element 104 is turned off and the second switching element 108 is turned on.

Under this condition, when the reception signal is fed through the antenna terminal 101 the first switching element 104 is in the OFF state and the impedance from the node 107 toward the transmission terminal 102 in the reception frequency band significantly increases by adjusting the phase using the phase—shift circuit 106 according to the amount of phase shift in the filter 105. Therefore, a reception signal fed through the antenna terminal 101 is transmitted to the reception terminal 103 through the node 107 and second switching element 108 without leaking into the transmission terminal 102.

(Second embodiment)

FIG. 2 shows a block diagram of a second embodiment of the transmitting/receiving switch according to the present invention.

The second embodiment of the transmitting/receiving switch according to the present invention comprises an antenna terminal 201 connected to an antenna ANT, a transmission terminal 202 connected to a transmission circuit Tx, a reception terminal 203 connected to a reception circuit Rx and a control terminal 204 connected to a control circuit CONT that performs alternate switching control.

The transmission terminal 202 is connected to the anode of a first diode 206 through a first capacitor 205 and the cathode of said first diode 206 is connected to the antenna terminal 201 through a low pass filter (LPF) 207 that passes a transmission signal, a phase-shift circuit 208, a node 209 and a second capacitor 210.

The antenna terminal 201 is also connected to the reception terminal 203 through the second capacitor 210, the node 209, a quarter-wave line 211 and a third capacitor 212.

The anode of the first diode 206 is also connected to the control terminal 204 through a coil 213 and a resistance 214. Midpoint between the coil 213 and the resistance 214 is grounded through a fourth capacitor 215.

The anode of a second diode 216 is connected to a midpoint between the quarter-wave line 211 and the third capacitor 212, and the cathode of said second diode 216 is grounded.

Referring to the transmitting/receiving switch shown in FIG. 2, in case of connecting the transmission circuit Tx with

the antenna ANT, a positive voltage is supplied from the control circuit CONT to the control terminal 204 that turns on the first and second diodes 206 and 216.

At this time, the direct-current component of the positive voltage supplied from the control terminal 204 is blocked by the first through fourth capacitors 205, 210, 212 and 215, and thus said positive voltage is supplied only on the partial circuit that includes the first and second diodes 206 and 216. Said positive voltage drives the first and second diodes 206 and 216 to the ON state.

The ON state of the first diode 206 lowers the impedance between the transmission terminal 202 and the antenna terminal 201 to establish a connection between them.

Meanwhile, the ON state of the second diode 216 brings the quarter—wave line 211 grounded at high frequencies and makes the impedance from the node 209 toward the reception terminal 203 significantly high. Therefore, an input signal from the transmission terminal 202 is transmitted to the antenna terminal 201 through the first capacitor 205, the first diode 206, the low pass filter 207, the phase—shift circuit 208, the node 209 and the second capacitor 210 without leaking into the reception terminal 203.

In this case, the low pass filter 207 attenuates harmonic contents in the transmission signal produced by the

distortion of the transmission signal due to the nonlinearity of the switching element of the first diode 206.

Also referring to the transmitting/receiving switch shown in FIG. 2, in case of connecting the reception circuit Rx with the antenna ANT, a null voltage is supplied from the control circuit CONT to the control terminal 204.

Under this condition, the first and second diodes 206 and 216 are driven to the OFF state and the impedance from the node 209 toward the transmission terminal 202 in the reception frequency band significantly increases by adjusting the phase using the phase-shift circuit 208 according to the amount of phase shift in the low pass filter 207. Therefore, a reception signal fed through the antenna terminal 201 is transmitted to the reception terminal 203 through the second capacitor 210, the node 209, the quarter-wave line 211 and the third capacitor 212 without leaking into the transmission terminal 202.

FIG. 3 shows a graph of the passing characteristics for transmission signals from the transmission terminal 202 to the antenna terminal 201 of the transmitting/receiving switch shown in FIG. 2.

In this graph, the vertical axis indicates an attenuation, S21 (dB), and the horizontal axis indicates a frequency normalized by the center frequency f(Tx0) of the transmission frequency band f(Tx), f/f(Tx0). In the transmission path

from the transmission terminal 202 to the antenna terminal 201, harmonic contents 2xf(Tx) and 3xf(Tx) of the transmission frequency band f(Tx) are attenuated according to the characteristics of the low pass filter 207. In addition, the reception frequency band f(Rx) is also attenuated.

FIG. 4 shows a graph of the impedance characteristics in the reception frequency band from the node 209 toward the transmission terminal 202 of the transmitting/receiving switch shown in FIG. 2.

As indicated in this graph, the impedance in the reception frequency band from the node 209 toward the transmission terminal 202 significantly increases by adjusting the phase using the phase-shift circuit 208 according to the amount of phase shift in the low pass filter 207.

concerning the differences between the first and second embodiments, the first and second switching elements 104 and 108 in the first embodiment are replaced by the first and second diodes 206 and 216 in the second embodiment, and the filter 105 in the first embodiment is replaced by the low pass filter 207 in the second embodiment. Either configuration provides the same effect of the present invention.

The phase-shift circuit 106 or 208 can be either a distributed constant line such as a strip line or a circuit

comprising lumped constant elements such as capacitor and/or coil.

By designing the filter 105 or 207 having phase characteristics that make the impedance from the node 107 or 209 toward the transmission terminal 102 or 202 in the reception frequency band significantly high, the same improved effect can be obtained without the phase-shift circuit 106 or 208.

Although, in the foregoing embodiments, the filter 105 and the low pass filter 207 are described to attenuate only harmonic contents of the transmission frequency, other spurious suppression can be realized with an appropriate configuration of the filter.

The transmitting/receiving switch of the present invention configured as described above attenuates harmonic contents in the transmission signal produced by the distortion of the transmission signal due to the non-linearity of the switching element, without any increase in the reception loss.

As described above, the transmitting/receiving switch according to the present invention comprises an antenna terminal connected to an antenna, a transmission terminal connected to a transmission circuit, a reception terminal connected to a reception circuit, first switching element that turns on or off the connection between the antenna

terminal and transmission terminal, second switching element that turns on or off the connection between the antenna terminal and the reception terminal and filtering means inserted between the first switching element and the antenna terminal. The filtering means attenuates harmonic contents in the transmission signal and has the phase characteristics that make the impedance band from the node between the antenna terminal and the second switching element toward the transmission terminal high in the reception frequency band.

By way of example, with the filtering means configured by a filter and a phase-shift circuit: at the time of transmission, harmonic contents in the transmission signal produced by the distortion of the transmission signal due to the non-linearity of the switching element can be suppressed; at the time of reception, the impedance in the reception frequency band toward the transmission terminal can be made significantly high by adjusting the phase-shift circuit according to the amount of phase shift in the filter; and thus alternate switching between transmission and reception can be realized without any increase in the reception loss.

A portable terminal unit or the like that operates with a telecommunication satellite as a relay station requires a high signal power at the time of transmission. Any of the above described embodiments implemented in such a portable terminal unit brings about further improved

telecommunication quality over the conventional techniques, because the transmitting/receiving switch of the present invention can attenuate harmonic contents in the transmission signal produced by the distortion of the transmission signal at the time of transmission due to the non-linearity of the switching element without any increase in the reception loss.

Preferable embodiment of such a portable terminal unit comprises an antenna for radio reception and radio transmission, any of the above described embodiments of the transmitting/receiving switch that alternately switches a signal transmission line for said radio reception or radio transmission, a transmission circuit connected to the transmission terminal of said transmitting/receiving switch and a reception circuit connected to the reception terminal of said transmitting/receiving switch.

As apparent from the above description, the present invention has an advantage as attenuate harmonic contents in the transmission signal produced by the distortion of the transmission signal at the time of transmission due to the non-linearity of the switching element without any increase in the reception loss.

What is claimed is:

A transmitting/receiving switch comprising:
 an antenna terminal connected to an antenna;

a transmission terminal connected to a transmission circuit;

a reception terminal connected to a reception circuit;

a first switching element that turns on or off the connection between said antenna terminal and said transmission terminal;

a second switching element that turns on or off the connection between said antenna terminal and said reception terminal; and

filtering means inserted between said first switching element and said antenna terminal,

wherein said filtering means (1) attenuates harmonic contents in the transmission signal and (2) has phase characteristics that increases the impedance from the node between said antenna terminal and said second switching element toward said transmission terminal in the reception frequency band.

- 2. The transmitting/receiving switch as set forth in claim
- 1, wherein said filtering means comprises:

a filter that attenuates harmonic contents in said transmission signal; and

a phase-shift circuit that performs phase adjustment on the basis of said phase characteristics according to the amount of phase shift caused by said filter.

- 3. The transmitting/receiving switch as set forth in claim 1, wherein said first switching element comprises a diode and said second switching element comprises a diode and a quarter-wave line.
- 4. The transmitting/receiving switch as set forth in claim 2, wherein said filter comprises a low pass filter that attenuates the harmonic contents in the transmission signal.
- 5. The transmitting/receiving switch as recited in claim 2, wherein said phase-shift circuit comprises either (1) a strip line or (2) a lumped constant circuit having a coil and/or capacitor.
- 6. The transmitting/receiving switch as set forth in claim 2, wherein said first switching element comprises a diode and said second switching element comprises a diode and a quarter-wave line.
- 7. A portable terminal unit comprising: an antenna for radio transmission and radio reception;

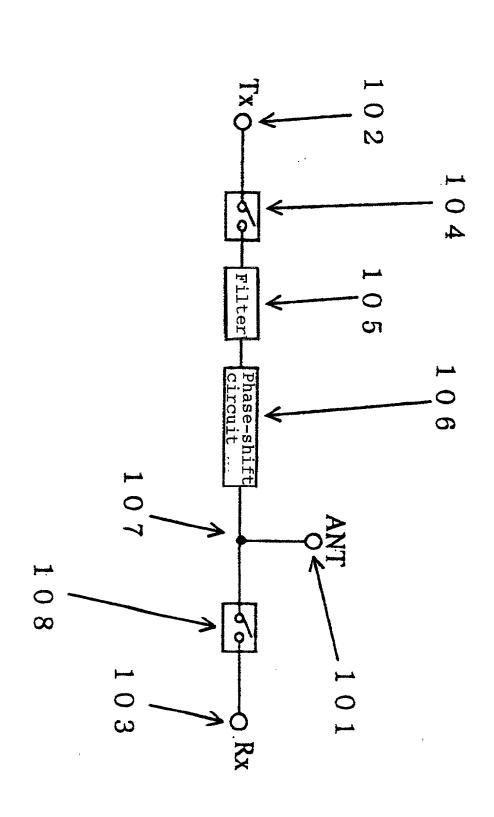
a transmitting/receiving switch as recited in claim 1 that switches transmission lines through which signals flow during said radio transmission or radio reception;

a transmission circuit connected to said transmission terminal of said transmitting/receiving switch; and

a reception circuit connected to said reception terminal of said transmitting/receiving switch.

ABSTRACT

A transmitting/receiving switch, in which a transmission terminal connected to a transmission circuit is connected to an antenna terminal through a first switching element, a filter for attenuating harmonic contents in a transmission signal, a phase-shift circuit for significantly increasing an impedance from a node toward said transmission terminal in a reception frequency band by phase-adjusting the amount of phase shift by performing phase adjustment according to the amount of the phase shift generated by said filter. Furthermore, the antenna terminal connected to an antenna ANT is connected to a reception terminal through the node and a second switching element, and the reception terminal is connected to a reception circuit Rx.



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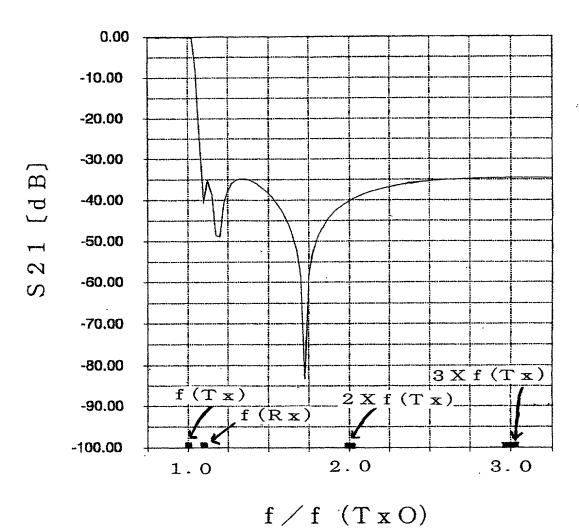


Fig. 4

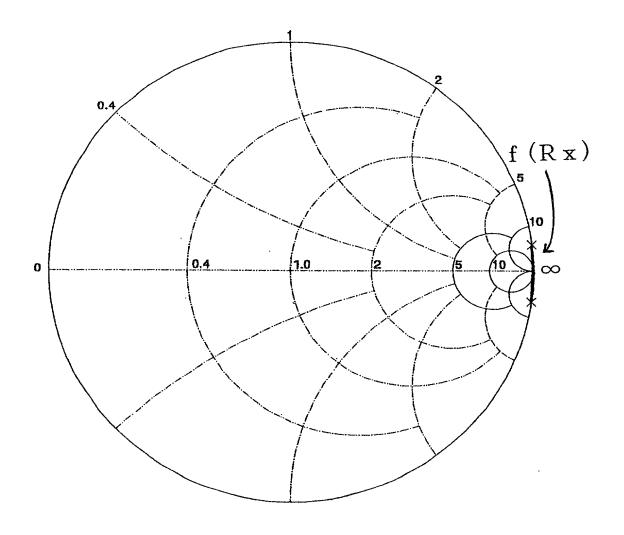
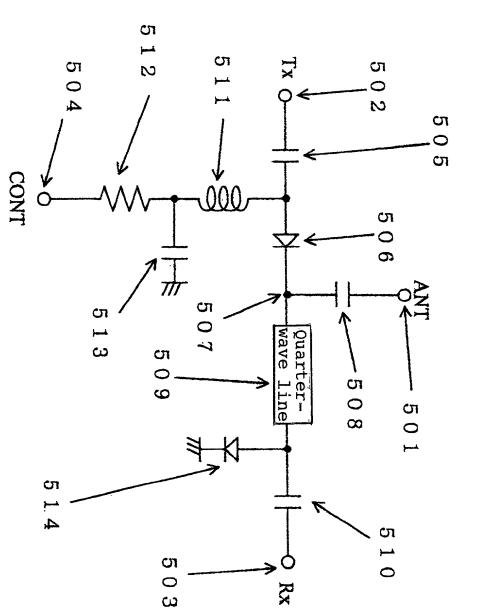


Fig. 5 PRIOR ART



Declaration and Power of Attorney For Patent Application English Language Declaration

As a below named inv	ventor, I hereby declare that:		
My residence, post of	fice address and citizenship a	re as stated below next	to my name,
first and joint inventor and for which a paten	inal, first and sole inventor (if (if plural names are listed be it is sought on the invention e CEIVING SWITCH AND POF	low) of the subject matte entitled	er which is claimed
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I hereby claim the ber listed below.	nefit under 35 U.S.C. § 119(e	e) of any United States p	rovisional application(s)
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agent(s) to prosecute this application and transact all business in the Patent and Trademark Officonnected therewith: Paul F. Prestia Reg. No. 23,031 Lawrence E. Ashery Reg. No. 34,515 Mark J. Marcelli Reg. No. 36,593 Allan Rather Reg. No. 19,717 Robert L. Anderson Reg. No. 25,771 Christopher J. Dervishian Reg. No. 24,248 Andrew L. Ney Reg. No. 31,549 Louis W. Beardell, Jr. Reg. No. 36,201 Kernneth N. Nigon Reg. No. 31,549 Louis W. Beardell, Jr. Reg. No. 40,506 Kernneth N. Nigon Reg. No. 32,117 Rocco L. Adomato Reg. No. 40,506 Kernneth N. Nigon Reg. No. 34,141 Jacques L. Etkowicz Reg. No. 41,738 Benjamin E. Leace Reg. No. 33,412 Jacques L. Etkowicz Reg. No. 41,738 Address all correspondence to: Rather & Prestia, Suite 301, One Westlakes, Berwyn, P.O. Box 980, Valley Forge, PA 19482-0980 Address all telephone calls to: at (610) 407-0700. I hereby declare that all statements made herein of my own knowledge are true and that statements made on information and belief are believed to be true; and further that the statements were made with the knowledge that willful false statements and the like made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of application or any patent issued thereon. Full name of sole or first inventor (given name, family name) Hiroyuki NAKAMURA Inventor's signature Date Reg.No. 42,48 Reg.No. 41,788 Reg.No. 41,	(Application Number	·)	(Filing Date)	(Status - pate	ented, pending, abandoned))
Allan Rather Andrew L. Ney Reg. No. 20,300 Christopher R. Lewis Reg. No. 36,201 Christopher J. Dervishian Reg. No. 42,481 Andrew L. Ney Reg. No. 31,549 Louis W. Beardell, Jr. Reg. No. 40,506 Revin R. Casey Reg. No. 32,117 Rocco L. Adornato Reg. No. 40,480 Reg. No. 40,480 Reg. No. 40,480 Reg. No. 40,480 Reg. No. 41,708 Reg. No. 41,708 Address all correspondence to: Rather & Prestia, Suite 301, One Westlakes, Berwyn, P.O. Box 980, Valley Forge, PA 19482-0980 Address all telephone calls to: at (610) 407-0700. I hereby declare that all statements made herein of my own knowledge are true and that statements made on information and belief are believed to be true; and further that the statements were made with the knowledge that willful false statements and the like made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of application or any patent issued thereon. Full name of sole or first inventor (given name, familly name) Hiroyuki NAKAMURA Inventor's signature Residence Katano-shi, Osaka JAPAN Citizenship JAPAN Post Office Address 5-3-1-206, Hoshida, Katano-shi, Osaka 571-0016 JAPAN Full name of second joint inventor, if any (given name, familly name) Makoto SAKAKURA	agent(s) to pros	secute this appl				
Ratner & Prestia, Suite 301, One Westlakes, Berwyn, P.O. Box 980, Valley Forge, PA 19482-0980 Address all telephone calls to: at (610) 407-0700. I hereby declare that all statements made herein of my own knowledge are true and that statements made on information and belief are believed to be true; and further that the statements were made with the knowledge that willful false statements and the like made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of application or any patent issued thereon. Full name of sole or first inventor (given name, family name) Hiroyuki NAKAMURA Inventor's signature Date Residence Katano-shi, Osaka JAPAN Citizenship JAPAN Post Office Address 5-3-1-206, Hoshida, Katano-shi, Osaka 571-0016 JAPAN Full name of second joint inventor, if any (given name, family name) Makoto SAKAKURA	Allan Ratner Andrew L. Ney Kenneth N. Nigon Kevin R. Casey Benjamin E. Leace	Reg.No. 19,717 Reg.No. 20,300 Reg.No. 31,549 Reg.No. 32,117 Reg.No. 33,412	Robert L. Anderson Christopher R. Lewis Louis W. Beardell, Jr. Rocco L. Adornato Jacques L. Etkowicz	Reg.No. 25,771 Reg.No. 36,201 Reg.No. 40,506 Reg.No. 40,480 Reg.No. 41,738		Reg.No. 36,593 Reg.No. 42,480
statements made on information and belief are believed to be true; and further that the statements were made with the knowledge that willful false statements and the like made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of a United States Code and that such willful false statements may jeopardize the validity of application or any patent issued thereon. Full name of sole or first inventor (given name, family name) Hiroyuki NAKAMURA Inventor's signature	Ratner & Prest	ia, Suite 301, Oi			, Valley Forge, PA 19	<u>482-0980</u>
Inventor's signature	statements ma statements we	ide on informa re made with	ation and belief ar	e believed to	be true; and further	er that these
Residence Katano-shi, Osaka JAPAN Citizenship JAPAN Post Office Address 5-3-1-206, Hoshida, Katano-shi, Osaka 571-0016 JAPAN Full name of second joint inventor, if any (given name, family name) Makoto SAKAKURA Second Inventor's signature	United States	Code and that	such willful false		Section 1001 of T	itle 18 of the
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Full name of third joint inventor, if any (given name, family name) Toshio I	
Third inventor's signature	Date
Third inventor's signature Residence Kobe-shi, Hyogo JAPAN	
Citizenship JAPAN	
Post Office Address 2-2-502, Okamoto 3-chome, Higashii 658-0072 JAPAN	nada-ku, Kobe-shi, Hyogo
Full name of fourth joint inventor, if any (given name, family name) Jyunich	ni YOSHIZUMI
Fourth inventor's signature Residence Nara-shi, Nara JAPAN	Date
Residence Nara-shi, Nara JAPAN	
Citizenship JAPAN	
Post Office Address 6-708-1-617, Gakuenyamatocho, Na	ra-shi, Nara 631-0041 JAPAN
Full name of fifth joint inventor, if any (given name, family name)	
Fifth inventor's signature	Date
Residence	
Citizenship	
Post Office Address	
Full name of sixth joint inventor, if any (given name, family name)	
Sixth inventor's signature	Date
Residence	
Citizenship	
Post Office Address	
Full name of seventh joint inventor, if any (given name, family name)	
Seventh inventor's signature	Date
Residence	
Citizenship	
Post Office Address	
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